



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/709,363	04/29/2004	Giovanni Gaviani	12693.0028.00US00	3362

23369 7590 07/13/2005

HOWREY LLP
C/O IP DOCKETING DEPARTMENT
2941 FAIRVIEW PARK DRIVE, SUITE 200
FALLS CHURCH, VA 22042-7195

EXAMINER

HOANG, JOHNNY H

ART UNIT	PAPER NUMBER
----------	--------------

3747

DATE MAILED: 07/13/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/709,363	Applicant(s) GAVIANI ET AL.	
	Examiner Johnny H. Hoang	Art Unit 3747	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 29 April 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 23 August 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>04/29/04, 09/21/04</u> | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Inventorship

1. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1, 2, and 4-6 are rejected under 35 U.S.C. 102(b) as being anticipated by Koch et al (US 5,367,665).

Regarding claim 1, the reference of Koch et al discloses the multi-processor system in a motor vehicle including the following subject matters:

the control system (10) with multiprocessor architecture system for an internal combustion power-train (see Fig. 1);

Art Unit: 3747

the control system (10) comprising a computing unit (34 or 36) capable of executing both basic control functions of the power-train and ancillary control functions not directly related to the control of the power-train (see Fig. 1);

the control system (10) being characterized in that the computing unit (34 or 36) comprises a main processor (12) dedicated to executing basic functions for controlling the power-train, at least one auxiliary processor (14) dedicated to executing ancillary control functions, a memory unit (16), a series of peripheral devices (42 or 44), at least one peripheral bus connection (38, 40), to which the peripheral devices (42 or 44) are connected, and a main bus connection (20) through a cross-bar switch (safety devices such as a switching relay) of the cross-bar bus type to allow the processors (12, 14) to communicate with the memory unit (16) and with the peripheral bus connection (38, 40) while avoiding the occurrence of conflicting communication operations (see Fig. 1, and col. 2, line 58 through col. 3, line 43).

Regarding claim 2, as discussed in claim 1.

Regarding claim 4, the reference of Koch et al further discloses the logic circuit (24) has an output line (28) which leads to the processors (12, 14) [see Fig. 1].

Regarding claims 5, and 6, as the same rejection of claim 1.

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 3, and 7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koch et al in view of Kraemer et al (US 5,454,095).

Art Unit: 3747

The reference of Koch et al discloses the invention all limitations of the independent claim but lacks the memory comprise either RAM type memory or ROM type memory. The reference of Kraemer et al teaches the memory is divided into at least two sectors, one in the read mode and one in the write mode (see abstract and claim 1). It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify and/or provide the system of Koch et al with two memory sectors, as taught by Kraemer et al, so as to facilitate for the processors access the memory in the same way at the same time.

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Johnny H. Hoang whose telephone number is (571) 272-4843. The examiner can normally be reached on Monday - Thursday (7:00Am-5: 30Pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Henry Yuen can be reached on (571) 272-4856.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JHH
July 9, 2005

Johnny H. Hoang
Examiner
Art Unit 3747

Tony M. Argonbright
Tony M. Argonbright
Primary Examiner
Art Unit 3747